Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((voltage with regulat$4 with suppl$4 with power) same controller same (circuit same board)) and @ad<"20030722" and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:08
L2	0	((signal with rout$5 with circuit$4) with (mux or multiplex$4) same stor$4) and @ad<"20030722" and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09
L3	0	(((rout$5 near circuit$4) with (mux or multiplex$4)) same storage) and @ad<"20030722" and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09
L4	0	((voltage with regulat$4 with suppl$4 with power) same controller same (circuit same board)) and @ad<"20030722" and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09
L5	0	(remov$5 same (signal with rout$5 with circuit) same component) and @ad<"20030722" and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09
L6	0	((signal with rout$5 with circuit$4) with (mux or multiplex$4) same apparatus) and @ad<"20030722" and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09
L7	9	((switch$5 or rout$5) same (system near board)) and @ad<"20030722" and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09
L8	6	((signal with rout$5 with circuit$4) same (mux or multiplex$4)) and @ad<"20030722" and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09

L9	4	((signal with rout$5 with circuit$4) with (mux or multiplex$4)) and @ad<"20030722" and (710/2.ccls. or 710/5.ccls. or 710/13.ccls. or 710/33.ccls. or 712/400.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:10
S1	6	((control$4 with circuit$3 with board) and (storage with array with circuit$4 with board) and (signal$3 with rout$5 with circuit$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 16:47
S2	1	((control$4 with circuit$3 with board) and (storage with array with circuit$4 with board) and (signal$3 with rout$5 with circuit$3)) and @ad<"20030722" and multiplex$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 18:21
S3	4	((control$4 with circuit$3 with board) and (storage with array with circuit$4 with board) and (signal$3 with rout$5 with circuit$3)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 18:25
S4	6	((circuit$3 with board) and (storage with array with board) and (signal$3 with rout$5 with circuit$3)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 18:42
S5	2	((circuit$3 with board) and (storage with array with board) and (signal$3 with rout$5 with circuit$3)) and @ad<"20030722" and (multiplex$4 or mux)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 18:53
S6	10	((circuit$3 with board) same (storage with board) same (signal$3 with rout$5)) and @ad<"20030722" and (multiplex$4 or mux)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 12:50
S7	1651	((signal with rout$5 with circuit$4) same (mux or multiplex$4)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09
S8	1158	((signal with rout$5 with circuit$4) with (mux or multiplex$4)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:10
S9	48	((signal with rout$5 with circuit$4) with (mux or multiplex$4) same apparatus) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09

S10	1	((signal with rout$5 with circuit$4) with (mux or multiplex$4) same apparatus same storage) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 12:54
S11	157	((signal with rout$5 with circuit$4) with (mux or multiplex$4) same stor$4) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:08
S12	1	((signal adj rout$5 adj circuit$4) with (mux or multiplex$4) same board) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 13:02
S13	44	((signal near3 rout$5 near3 circuit$4) with (mux or multiplex$4) same memory) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 13:05
S14	3	((signal adj rout$5 adj circuit$4) with (mux or multiplex$4) same memory) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 13:03
S15	3	((signal$4 adj rout$5 adj circuit$4) with (mux or multiplex$4) same (memory or stor$5)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 13:04
S16	3	((signal$4 adj rout$5 adj circuit$4) same (mux or multiplex$4) same (memory or stor$5)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 13:04
S17	4	((signal near rout$5 near circuit$4) with (mux or multiplex$4) same memory) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 17:16
S18	16	((rout$5 near circuit$4) with (mux or multiplex$4) same storage) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 15:02
S19	16	(((rout$5 near circuit$4) with (mux or multiplex$4)) same storage) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09

S20	0	(((rout$5 near circuit$4) with (mux or multiplex$4)) same storage same board) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 13:10
S21	0	(((rout$5 near circuit$4) with (mux or multiplex$4)) same storage same pcb) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 13:10
S24	0	((rout$5 near circuit$4) same (mux or multiplex$4) same (storage or memory)) and @ad<"20030722" and ("5729763".pn. or "6052278".pn. or "6274807".pn. or "6498723". pn. or "6517358".pn. or "6567879".pn. or "20020071192" or "20020132588" or "20030028698")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 15:04
S25	0	((rout$5 with circuit$4) same (mux or multiplex$4) same (storage or memory)) and @ad<"20030722" and ("5729763".pn. or "6052278".pn. or "6274807".pn. or "6498723". pn. or "6517358".pn. or "6567879".pn. or "20020071192" or "20020132588" or "20030028698")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 17:12
S28	0	((rout$5 near circuit$4) same (system near circuit near board)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 17:19
S29	4	((rout$5 near circuit$4) same (system near board)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 17:20
S30	1474	((switch$5 or rout$5) same (system near board)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09
S31	114	((switch$5 or rout$5) same (system near circuit near board)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 17:21

S32	83	(((switch$5 or rout$5) with circuit$4) same (system near circuit near board)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 17:22
S33	12	(((switch$5 or rout$5) with circuit$4) same (system near circuit near board) same coupl$4) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 17:25
S34	7	((rout$5 with circuit$4) same (system near circuit near board) same coupl$4) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 18:14
S41	22	((rout$5 with circuit$4) same (supply with power) same controller same board) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 18:15
S42	1	((rout$5 with circuit$4) same (supply with power) same controller same board same array) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 18:15
S48	2882	(voltage same regulat$4 same suppl$4 same power same controller same (circuit or board)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 13:39
S49	109	((voltage with regulat$4 with suppl$4 with power) same controller same (circuit same board)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09
S50	26	(((voltage adj regulat$4) with suppl$4 with power) same (controller with (circuit with board))) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 15:40
S54	13	((signal with rout$5) same ((I/O adj controller) with between)) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 17:57
S61	11	(housing same remov$5 same (signal with rout$5) same component) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 18:23

S62	122	(remov$5 same (signal with rout$5 with circuit) same component) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 14:09
S63	22	(remov$5 with (signal with rout$5 with circuit) with component) and @ad<"20030722"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 18:27
S64	29	(remov$5 with (signal with rout$5 with circuit)) and @ad<"20030722" and housing	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 18:27